	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	IS&R	L1	1	("20020183990").PN.	USPAT; US-PGPUB	2003/02/24 11:02		
2	BRS	L4	2	switch same model same impedance same conductance and input same output	USPAT; US-PGPUB; DERWENT	2003/02/24 11:04		
3	BRS	L8	98	switch same model same impedance same input same output	USPAT; US-PGPUB; DERWENT	2003/02/24 11:09		
4	BRS	L16	309	switch same model same input same output and "I/O"	USPAT; US-PGPUB; DERWENT	2003/02/24 11:11		
5	BRS	L12	309	switch same model same input same output and I/O	USPAT; US-PGPUB; DERWENT	2003/02/24 11:19		
6	BRS	L20	178	circuit same model same I/O same ports	USPAT; US-PGPUB; DERWENT	2003/02/24 11:20		

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	Type	Hits	Search Text	DBs	Time Stamp
17	BRS	1911	model and (integrated adj circuit) same switching	USPAT; US-PGPUB; DERWENT	2003/02/22 17:37
18	BRS	235	model and (integrated adj circuit) same switching same voltage same current and resistors	USPAT; US-PGPUB; DERWENT	2003/02/22 17:39
19	BRS	304	model and (integrated adj circuit) same switching same voltage same current	USPAT; US-PGPUB; DERWENT	2003/02/22 17:39
20	BRS	235	model and (integrated adj circuit) same switching same voltage same current and (resistors or resisters)	USPAT; US-PGPUB; DERWENT	2003/02/22 17:39
21	BRS	91	model and (integrated adj circuit) same switching same voltage same current and resistors and waveforms	USPAT; US-PGPUB; DERWENT	2003/02/22 17:42
22	BRS	45	model and (integrated adj circuit) same switching same voltage same current same resistors and waveforms	USPAT; US-PGPUB; DERWENT	2003/02/22 17:50
23	BRS	443	model and modeling and input same output and (integrated adj circuit) and characterization	USPAT; US-PGPUB; DERWENT	2003/02/22 17:51
24	BRS	15451	driver same circuit and (integrated adj circuit) and switching	USPAT; US-PGPUB; DERWENT	2003/02/22 17:52
25	BRS	331	driver same circuit same model and (integrated adj circuit) and switching	USPAT; US-PGPUB; DERWENT	2003/02/22 17:52
26	BRS	180	model same (integrated adj circuit) same switching	USPAT; US-PGPUB; DERWENT	2003/02/22 17:59
27	BRS	48	model same (integrated adj circuit) same switching same resistor	USPAT; US-PGPUB; DERWENT	2003/02/22 18:03

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	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
65	BRS	312	(IO or I/O) same model same circuit and switching	USPAT; US-PGPUB; DERWENT; IBM_TDB	2003/08/06 14:16		
66	BRS	193	(IO or I/O) same model same circuit and switching and driver	USPAT; US-PGPUB; DERWENT; IBM_TDB	2003/08/07 10:26		

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1 **Extraction of transient behavioral model of digital I/O buffers from IBIS**
Tehrani, P.F.; Yuzbe Chen; Jiayuan Fang;
Electronic Components and Technology Conference, 1996.
Proceedings., 46th 28-31 May 1996
Page(s): 1009 -1015

[Abstract]   [PDF Full-Text (500 KB)] **IEEE CNF**

---

2 **The development of analog SPICE behavioral model based on IBIS model**
Ying Wang; Han Ngee Tan;
VLSI, 1999. Proceedings. Ninth Great Lakes Symposium on , 4-6 March 1999
Page(s): 101 -104

[Abstract]   [PDF Full-Text (60 KB)] **IEEE CNF**

---

3 **An experimental procedure to derive reliable IBIS models**
Zak, T.; Ducrot, M.; Xavier, C.; Drissi, M.;
Electronics Packaging Technology Conference, 2000. (EPTC 2000).
Proceedings of 3rd , 5-7 Dec 2000
Page(s): 339 -344

[Abstract]   [PDF Full-Text (612 KB)] **IEEE CNF**

---

4 **Behavioral modeling for timing, noise, and signal integrity analysis**

Hayes, J.D.; Wissel, L.;
Custom Integrated Circuits, 2001, IEEE Conference on. 6-9 May
2001
Page(s): 353 -356

[Abstract]   [PDF Full-Text (368 KB)] **IEEE CNF**

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